



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa, Mark N. Yamashita and Yuval Bachar

Assignee: Cisco Technology, Inc.

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK BETWEEN VIAS IN A PRINTED CIRCUIT BOARD

Application No.: 10/727,381 Filing Date: December 4, 2003

Examiner: Jeremy C. Norris Group Art Unit: 2841

Docket No.: CIS0203US

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Austin, Texas  
June 21, 2006

MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P. O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**RESPONSE TO NON-FINAL OFFICE ACTION**

Dear Sir:

This paper is responsive to the Office Action dated March 21, 2006, having a shortened statutory period expiring on June 21, 2006. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the claim listing which begins on page 5 of this paper.

Amendments to the Drawings begin on page 14 of this paper.

Remarks begin on page 15 of this paper.